MSP430F2121IRGER

	MSP430F2121IRGER
	:TI :MSP430F2121IRGER :1231+
	1125
	0755-23605436 13510211655

????msp430????????????????????????????????????,????????,?????????????????????????????16?risc cpu?16????,??????????????????(dco)?????????????1μs?????
msp430x21x1???????????????????????16????,????????,???i / o???
??????????????????,????????,???a / d?????

low supply voltage range: 1.8 v to 3.6 v ultra-low power consumption active mode: 250 μa at 1 mhz, 2.2 v standby mode: 0.7 μa off mode (ram retention): 0.1 μa ultra-fast wake-up from standby mode in

less than 1 µs 16-bit risc architecture, 62.5-ns instruction cycle time basic clock module configurations internal frequencies up to 16 mhz with four calibrated frequencies to ±1% 32-khz crystal high-frequency crystal up to 16 mhz resonator external digital clock source 16-bit timer_a with three capture/compare registers on-chip comparator for analog signal compare function or slope analog-to-digital (a/d) conversion brownout detector serial onboard programming, no external programming voltage needed, programmable code protection by security fuse bootstrap loader on chip emulation module family members: msp430f2101 1kb + 256b flash memory 128b ram msp430f2111 2kb + 256b flash memory 128b ram msp430f2121 4kb + 256b flash memory 256b ram msp430f2131 8kb + 256b flash memory 256b ram available in a 20-pin plastic small-outline wide body (sowb) package, 20-pin plastic small-outline thin (tssop) package, 20-pin tvsop package, and 24-pin qfn package for complete module descriptions, see the msp430x2xx family user's guide (slau144)