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the sn8p2711a is a risc-like high performance and low power consumption 8-bit micro-controller. one clock per instruction cycle (1t) architecture will provide up to 16mips computation power. also, the high eft (electrical fast transients) protection capability let this device is suitable for high noisy industrial application. the device is designed with the excellent ic structure including the program memory up to 1k-word otp rom, data memory of 64-bytes ram, two 8-bit timer counters (tc0, tc1), a watchdog timer, five interrupt sources (tc0, tc1, adc, int0, int1), an 5+1 channel 12-bit adc converter (one internal adc channel for vdd measurement), two channels pwm output (pwm0, pwm1), two channels buzzer output (bz0, bz1) and 4-level stack buffers. there are four oscillator configurations to select for generating system clock, including high/low speed crystal, ceramic resonator or cost-saving rc. this device also includes an internal 16mhz rc oscillator for system clock and includes an internal low frequency rc oscillator for slow mode controlled by programming.

operating voltage: 2.4v~5.5v at fosc = 4mhzrom size: 1k * 16 bitsram size: 64 * 8 bits4-levels stack bufferbi-directional i/o: p0, p4, p5level trigger wakeup input port: p0p4 pins shared with adcexternal interrupt: p0two external interrupts: int0, int1three internal interrupts: tc0, tc1, adcon-chip watch dog5+1 channels 12-bit adcinternal high clock: 16mhz rc oscillatorexternal high clock: crystal type up to 16mhzinternal low clock: rc oscillator, about 16khz at 3v, 32khz at 5vone clock per instruction cyclemost of instructions are one cycle onlyall rom area lookup table function(movc)

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